

To search page

1971

Development of 36-pin packages

for the Super High-Speed Computer Project

~ Packaging ~

Around 1970, Japan's first national project for computer development, "Ultra High-Performance Computer Development", was launched, and Hitachi Central Research Laboratory was responsible for the development of the logic LSIs. 10 digital ICs were mounted on an alumina glazed substrate with Al multilayer interconnects, which were connected by Al-Al ultrasonic bonding, thus forming one LSI, and it was delivered to Nippon Telegraph and Telephone Public Corporation (now NTT).

